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Docket No.: 8733.935.00-US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Soon Sung YOO et al.

Confirmation No. 7322

Application No.: 10/705,913

Customer No.: 30827

Filed: November 13, 2003

Art Unit: 2826

For: THIN FILM TRANSISTOR ARRAY
SUBSTRATE AND MANUFACTURING
METHOD OF THE SAME

Examiner: Kevin V. Quinto

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Dear Sirs:

In response to the Election/Restriction Requirement set forth in the Office Action mailed November 4, 2005, Applicant hereby provisionally elects Group II - Claims 3-7 and 10-14 drawn to a method of manufacturing a thin film transistor array substrate, without prejudice or disclaimer.

An action on the merits is respectfully requested.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Application No.: 10/705,913

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Dated: December 2, 2005

Respectfully submitted,

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